

OCT 07 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

EDWARD C. STEWART

Group Art Unit: 2125

Serial No.: 10/085,829

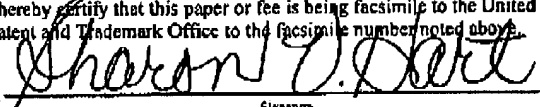
Examiner: Alexander J. Rosowski

Filed: FEBRUARY 28, 2002

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For: ANALYZING ERROR SIGNALS
BASED ON FAULT
DETECTIONAtty. Dkt.: 2000.087300/TT4580
CUSTOMER NO.: 23720**RESPONSE TO OFFICE ACTION MAILED SEPTEMBER 29, 2004**Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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This paper is submitted in response to the final Office Action mailed September 29, 2004 for which the shortened three-month statutory response date is December 29, 2004. However, since this paper is being submitted on October 7, 2004, this response is filed within two months of the mailing date of the Office Action dated September 29, 2004.

If an extension of time is required to enable this paper to be timely filed and there is no separate Petition for Extension of Time filed herewith, this paper is to be construed as also constituting a Petition for Extension of Time Under 37 CFR § 1.136(a) for a period of time sufficient to enable this document to be timely filed.

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FROM: SHARON V. HART
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10/085,829

DOCUMENT: RESPONSE TO OFFICE
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2000.087300/TT4580

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NOTES/COMMENTS:

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No fee is believed due as a result of the response contained herein. However, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be required for any reason, the Commissioner is authorized to deduct said fees from Advanced Micro Devices, Inc. Deposit Account No. 01-0365/TT4580/2000.087300. In the event the monies in that account are insufficient, the Commissioner is authorized to withdraw funds from Williams, Morgan & Amerson, P.C. Deposit Account No. 50-0786/2000.087300.